

36J.P191

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

DENNY M. LIN

Application No.: 09/267

Filed: March 15, 1999

For: REDUCING DISCONTINUITIES
IN SEGMENTED IMAGING
SENSORS

Group Art Unit: 2722

May 11, 1999

The Assistant Commissioner for Patents
Washington, D.C. 20231

LETTER TRANSMITTING FORMAL DRAWINGS

Sir:

Transmitted herewith are formal drawings to be
substituted for the corresponding drawing sheets presently on
file in the above-identified application.

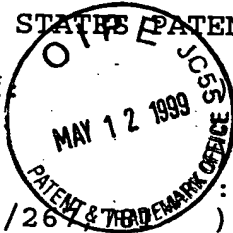
Applicant's undersigned attorney may be reached in
our California Office by telephone at (714) 540-8700. All
correspondence should continue to be directed to our below
listed address.

Respectfully submitted,

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The diagram illustrates a parallel processing system for image data. It begins with an input (1) that splits into three parallel paths (2). Each path enters a large processing block (3) which is divided into three segments: SEGMENT A, SEGMENT B, and SEGMENT C. These segments are connected to three parallel processing pipelines: PIPELINE A, PIPELINE B, and PIPELINE C. Each pipeline is followed by an A/D CONVERTER (21, 23, 25) and then a PROCESSOR (22, 24, 26). The outputs of these processors are fed into a RECOMBINATION PROCESSOR (28), which then outputs to an IMAGE STORAGE unit (29). The diagram includes various reference numerals (1 through 34) indicating specific components and connections.

FIG. 1

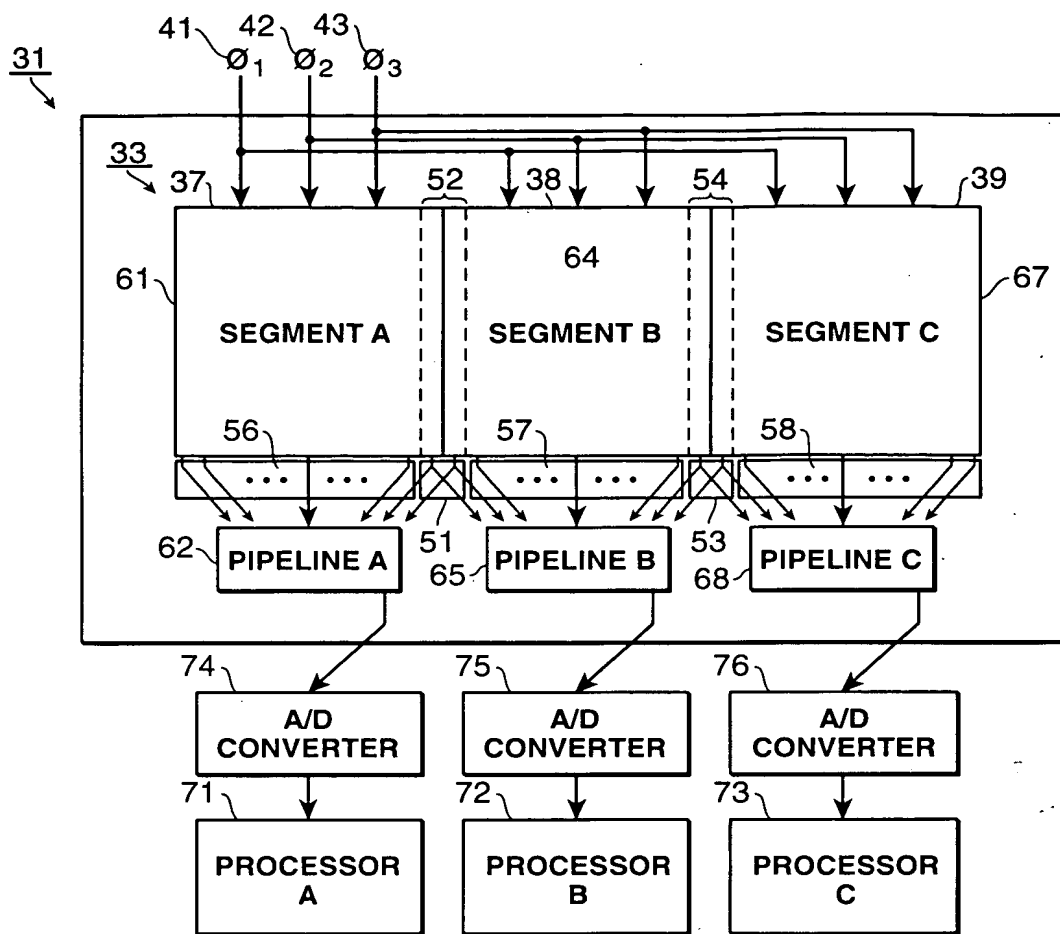


FIG. 2

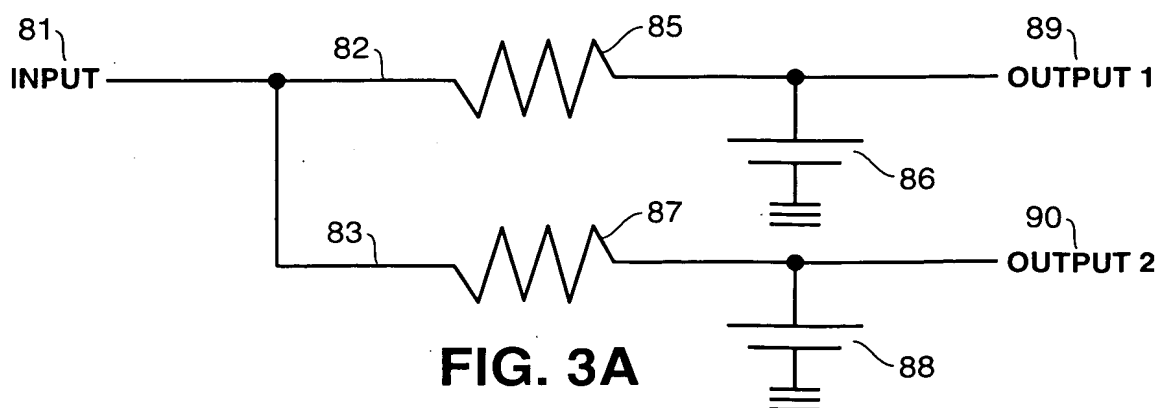


FIG. 3A

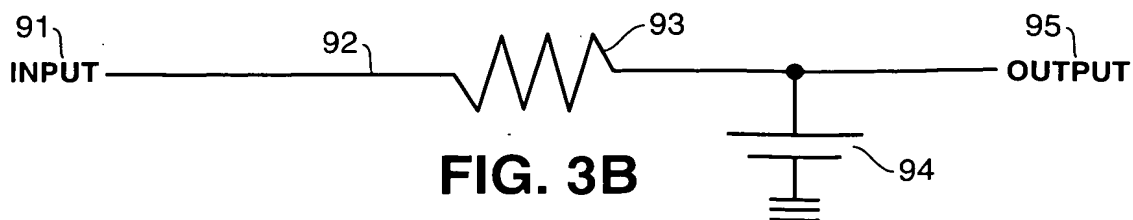


FIG. 3B

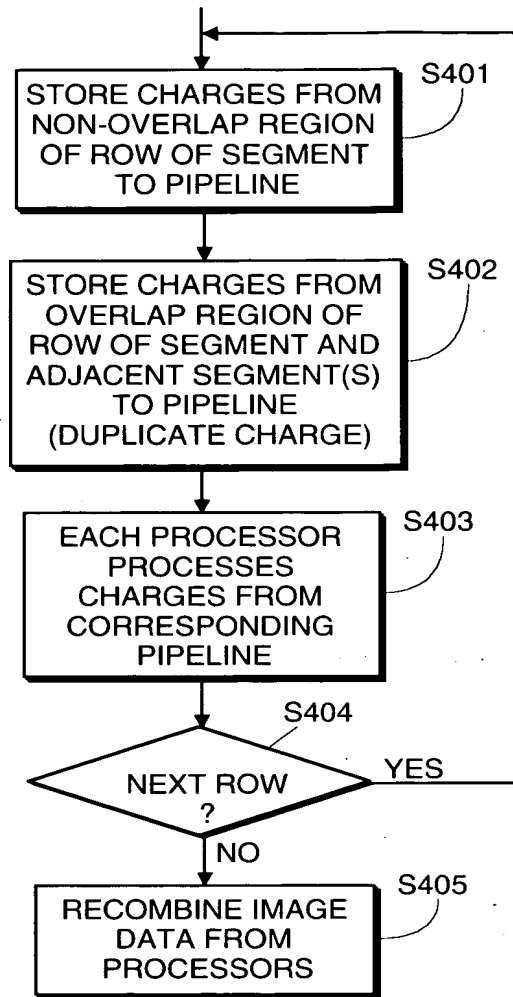


FIG. 4

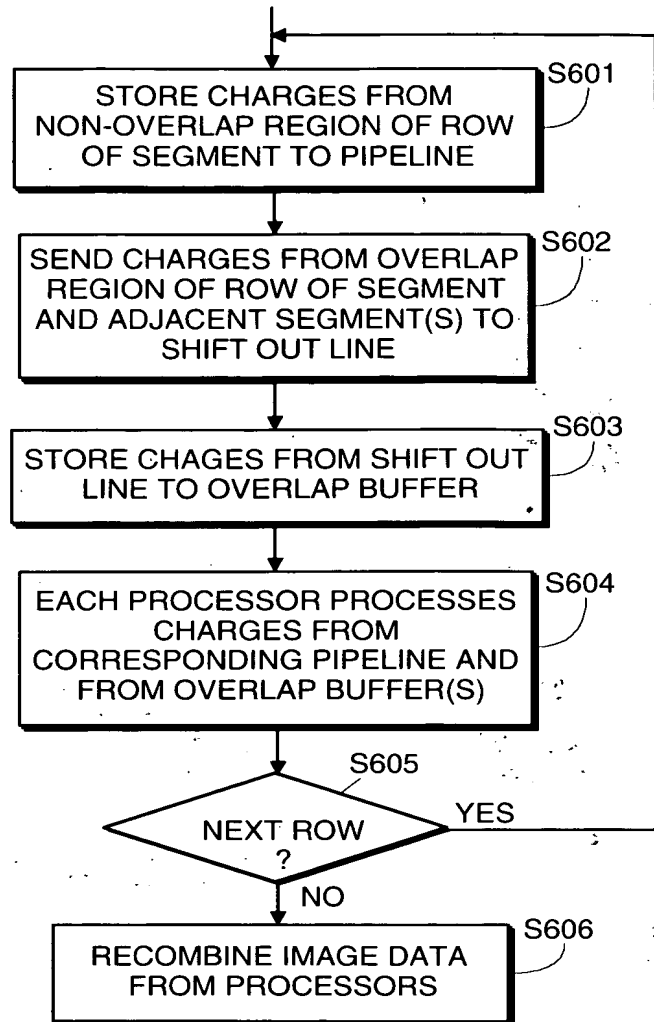


FIG. 6

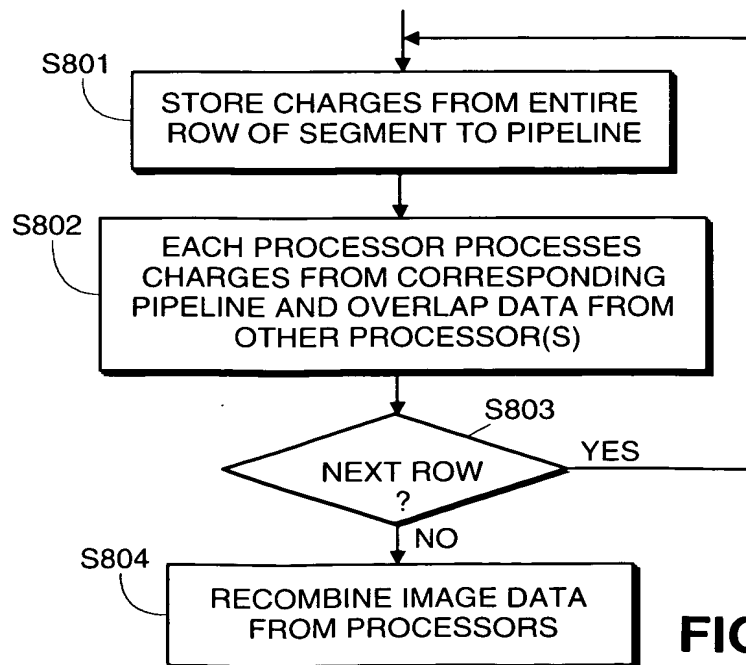


FIG. 8

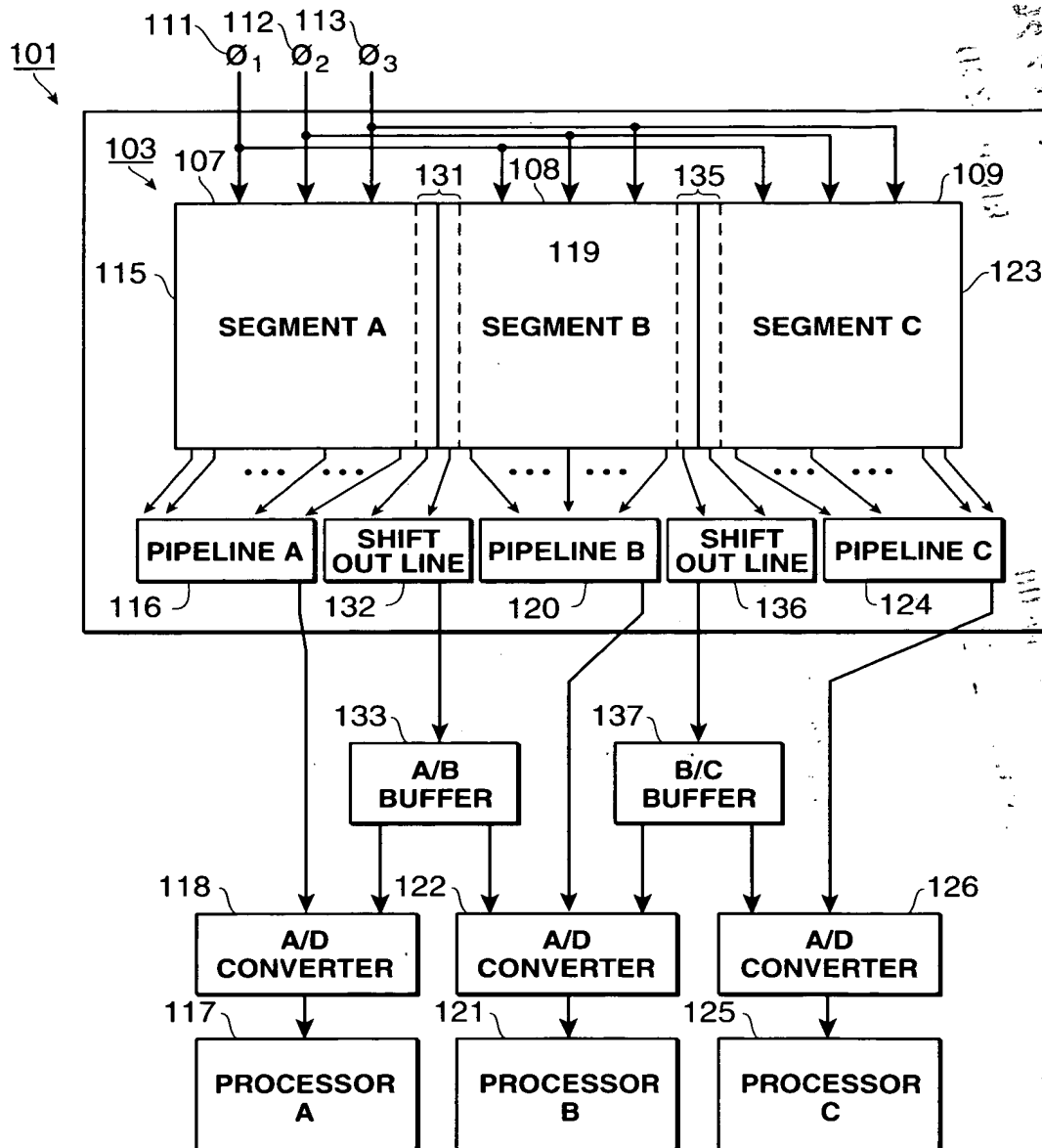


FIG. 5

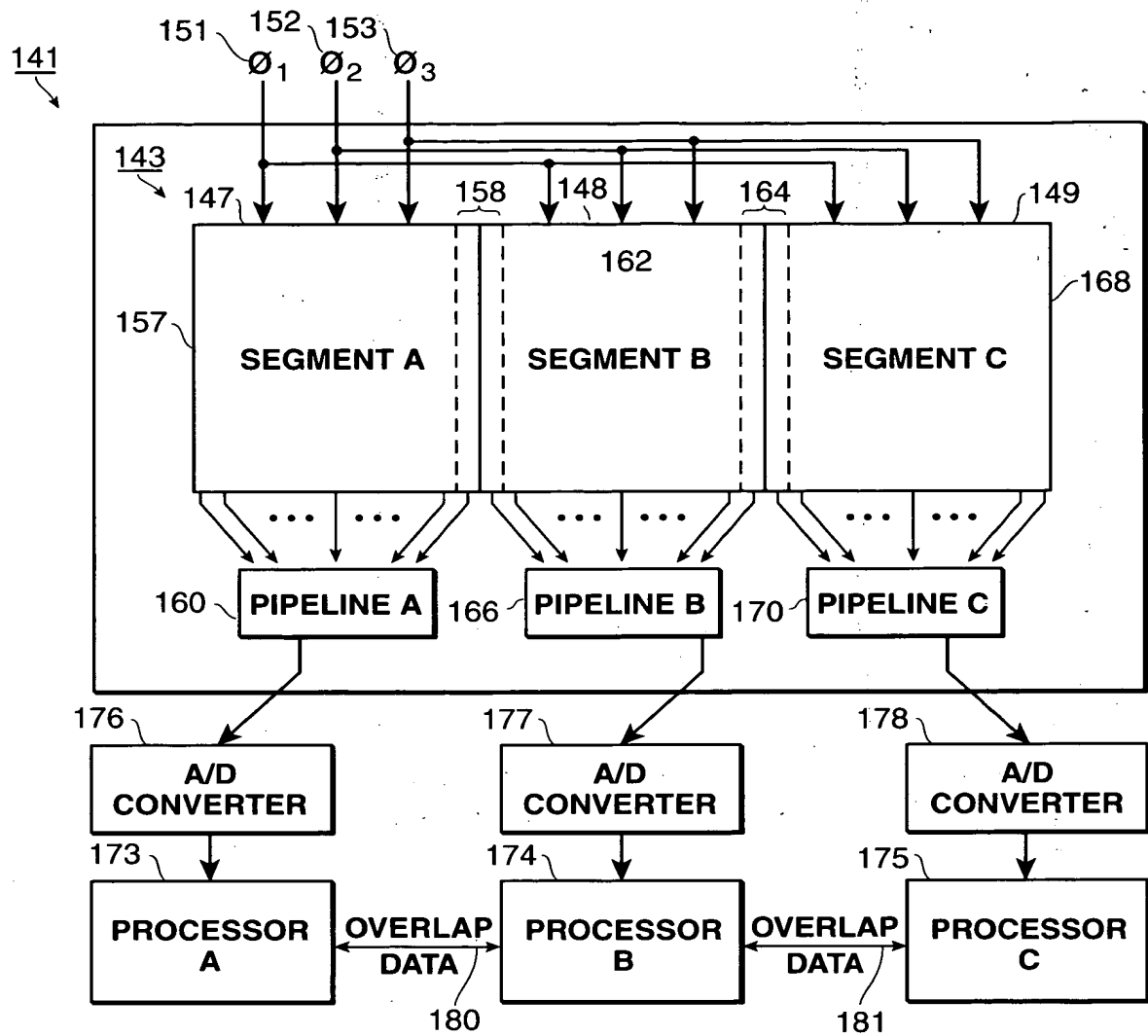


FIG. 7